

# Tracking: Winter Conference Development

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**Tracking Meeting**

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# Tracking Development: Progress

Projects in for 5.1.0(5.0.0int4 built - may be CT tracking mem leak)

- New COT alignment: done
- Corresponding silicon alignment: second version ready: first pass L00 and ISL done
- Improved COT drift model: code in place: need to run calibration job
- t0 used in COT tracking: t0 in Z vertex collection and code exists for fitting
- IO tracking: inside-out: working version ready
- Standalone tracking improvements wafer level corrections: done
- Standalone tracking expected ladder information: First version in: needs calibration table and accessors
- Beamline code run in or before production: working version ready: needs testing
- Beamline pointing for HL code: needs testing
- Improvements to dE/dx code: done
- Phoenix tracking: complete version ready: need to combine with IO tracking
- L3 Silicon Standalone Tracking: First version ready: need to quantify performance

# Tracking Development: Progress

## In progress

- COT HL/SL track merge: needs study
- PADTracks cuts for IO tracks: needs study
- Improvements to pull distributions of track parameters: needs more manpower
- Use of L00: Need to write refit that uses L00
- L3 Silicon Standalone Tracking: Need to quantify performance
- Next version COT alignment: in progress