

Tracking: Winter Conference Development

**Matthew Herndon
Johns Hopkins University**

Tracking Meeting

July 30, 2003

Tracking Development: Progress

Projects in for 5.1.0

5.1.0pre2 built - Working on memory leak - silicon efficiency is low

- New COT alignment: done
- Corresponding silicon alignment: final version ready
- Improved COT drift model: code in place: need to determine constants/run calibration job
- t0 used in COT tracking: t0 in Z vertex collection and code exists for fitting
- IO tracking: inside-out: working version ready: working on track refit mechanism
Clean up of COT/IO-Standalone duplicates
- Standalone tracking improvements wafer level corrections: done
- Standalone tracking expected ladder information: First version in: needs calibration table and accessors
- Beamline code run in or before production: working version ready: needs testing
- Beamline pointing for HL code: needs testing
- Improvements to dE/dx code: done
- Phoenix tracking: complete version ready: need to combine with IO tracking
- L3 Silicon Standalone Tracking: First version ready: need to quantify performance

Tracking Development: Progress

In progress

- COT HL/SL track merge: **needs study and new code**
- PADTracks cuts for IO tracks: **needs study and new code**
- Improvements to pull distributions of track parameters: **needs more manpower**
- Use of L00: **Need to write refit that uses L00**
- Next version COT alignment: **in progress**