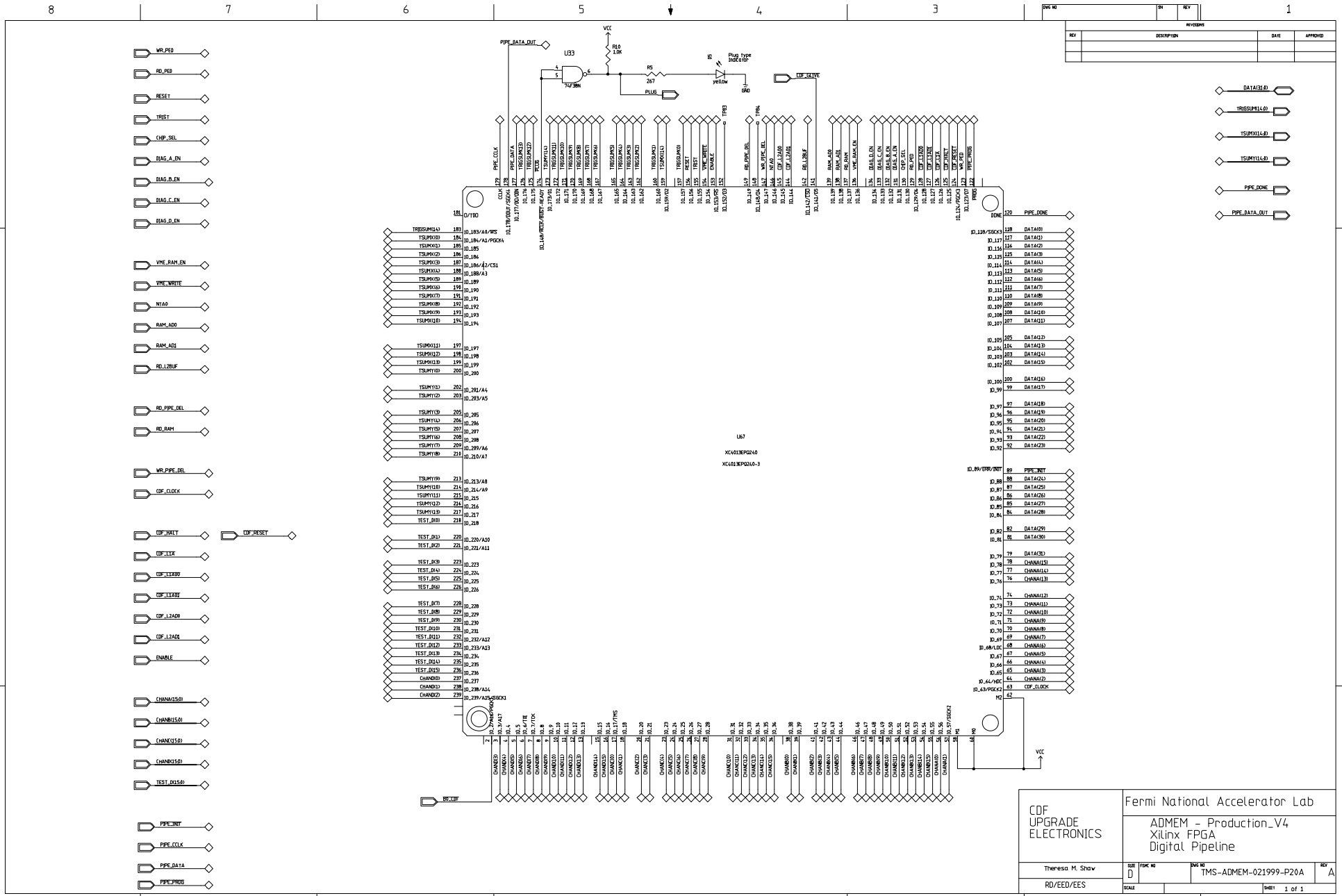

REV	DESCRIPTION	DATE	APPROVED

CDF UPGRADE ELECTRONICS

Fermi National Accelerator Lab

ADMEM - Production_V4
Xilinx FPGA
Digital Pipeline

Theresa M. Shaw

RD/EED/EES

SIZE D | FSCM NO | DWG NO TMS-ADMEM-021999-P20A | REV A

SCALE | SHEET 1 of 1