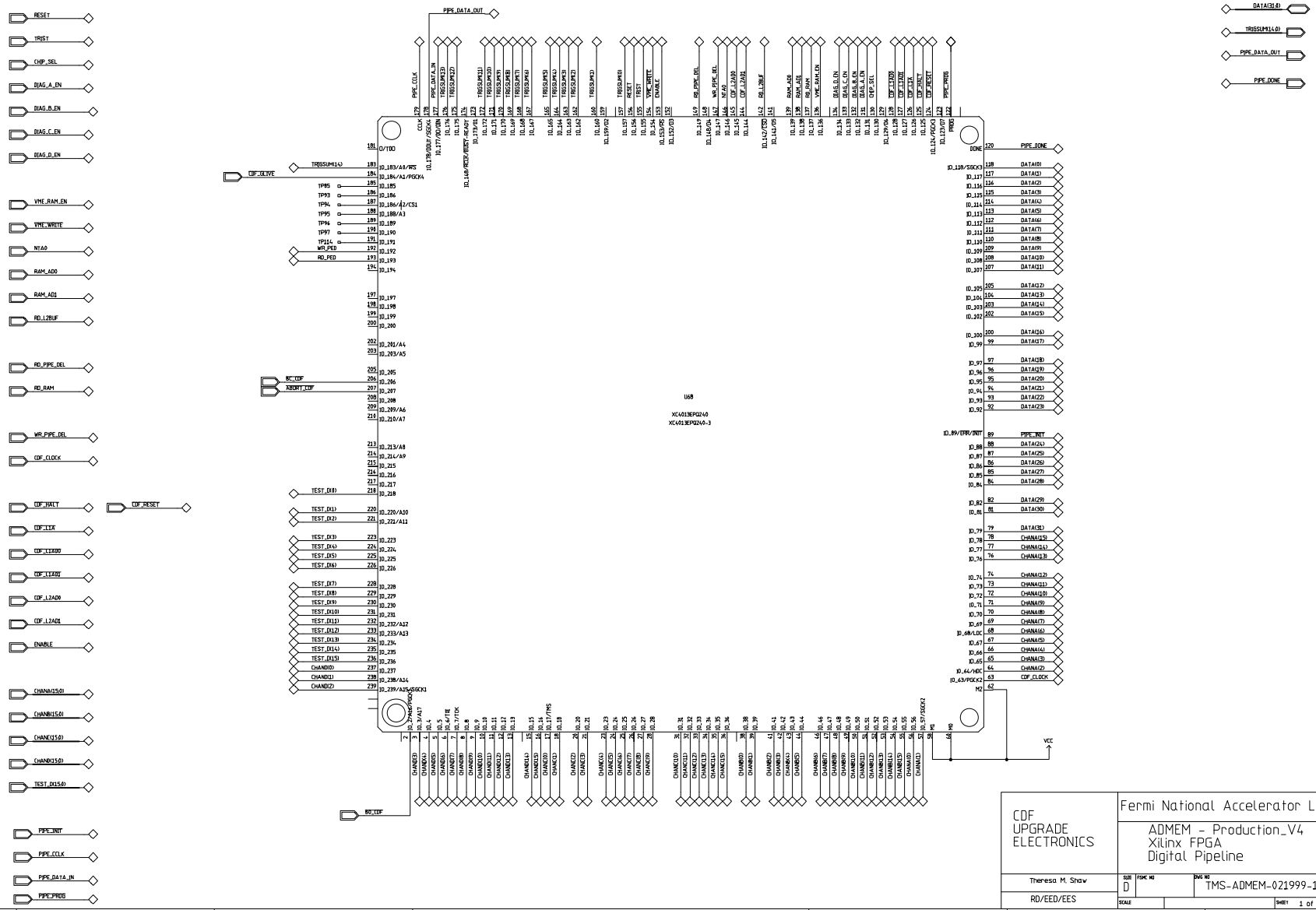
DWG NO	SH	REV

REVISIONS

REV	DESCRIPTION	DATE	APPROVED



CDF UPGRADE ELECTRONICS	Fermi National Accelerator Lab
	ADMEM - Production_V4 Xilinx FPGA Digital Pipeline
Theresa M. Shaw	SIZE D · FSCM NO · DWG NO TMS-ADMEM-021999-19A · REV A
RD/EED/EES	SCALE · SHEET 1 of 1