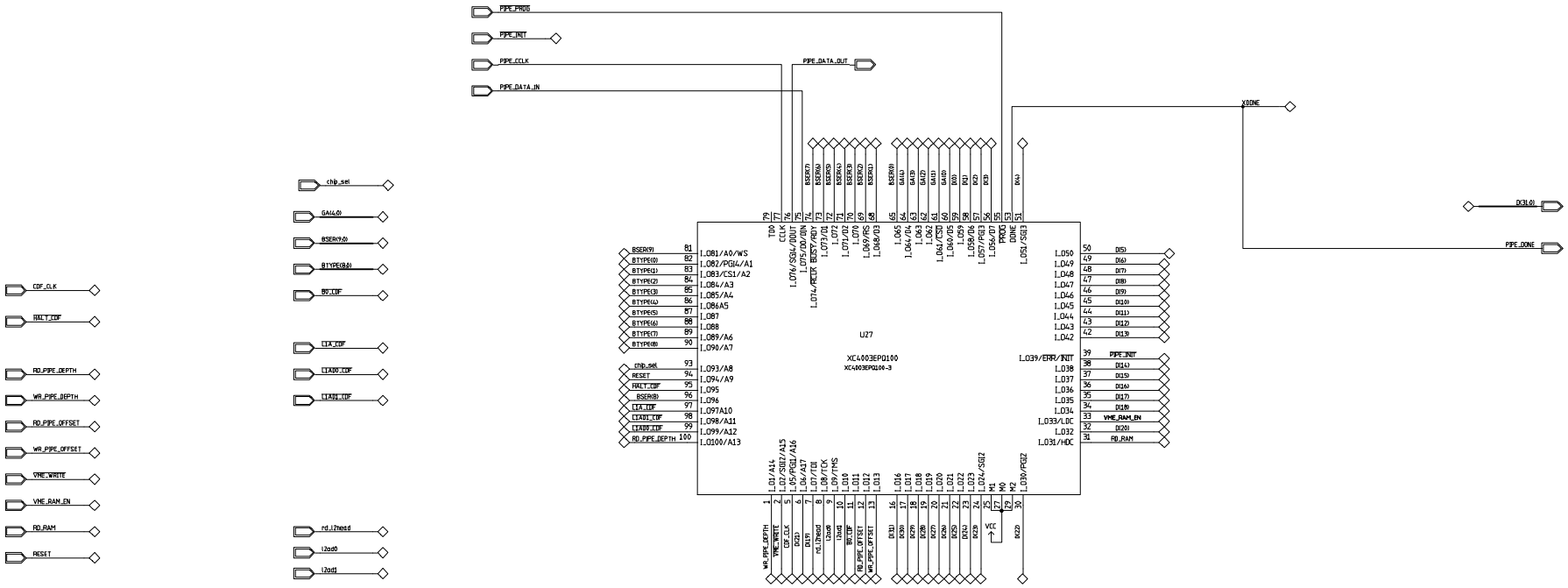

DWG NO	SH	REV

REVISIONS

REV	DESCRIPTION	DATE	APPROVED

CDF UPGRADE ELECTRONICS

Fermi National Accelerator Lab

ADMEM - Production_V4
Xilinx FPGA
L2 Header Word

Theresa M. Shaw

RD/EED/EES

SIZE	FSCM NO	DWG NO	REV
D		TMS-ADMEM-021999-P16B	A

SCALE

SHEET 1 of 1