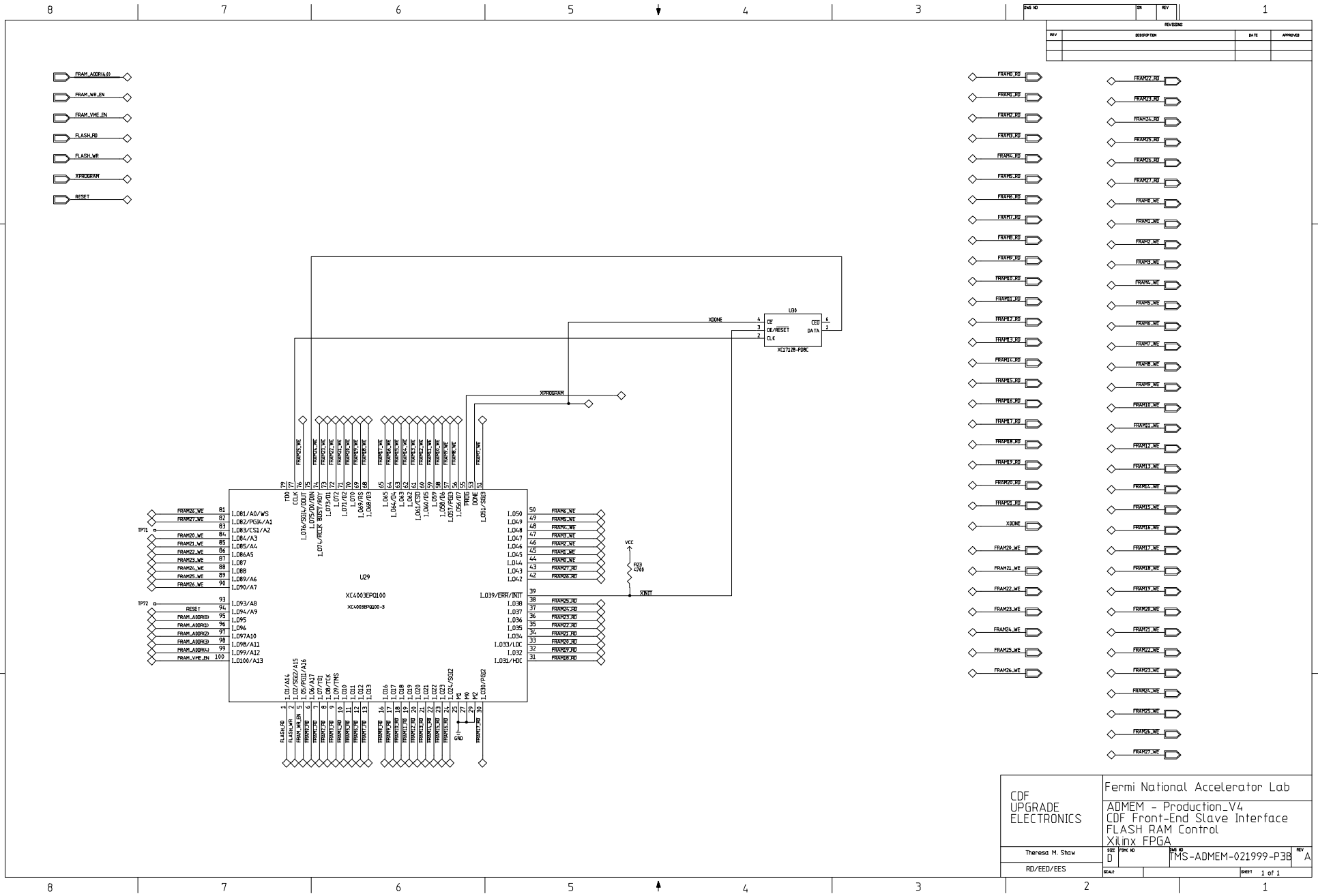

FRAM_ADDR(4,0)
FRAM_WR_EN
FRAM_VME_EN
FLASH_RD
FLASH_WR
XPROGRAM
RESET
U30
XC17128-PD8C
XDONE
XPROGRAM
U29
XC4003EPQ100
XC4003EPQ100-3
VCC
R23
4700
XINIT
TP71
TP72
GND
CDF
UPGRADE
ELECTRONICS
Fermi National Accelerator Lab
ADMEM - Production_V4
CDF Front-End Slave Interface
FLASH RAM Control
Xilinx FPGA
Theresa M. Shaw
RD/EED/EES
SIZE D
DWG NO TMS-ADMEM-021999-P3B
REV A
SHEET 1 of 1