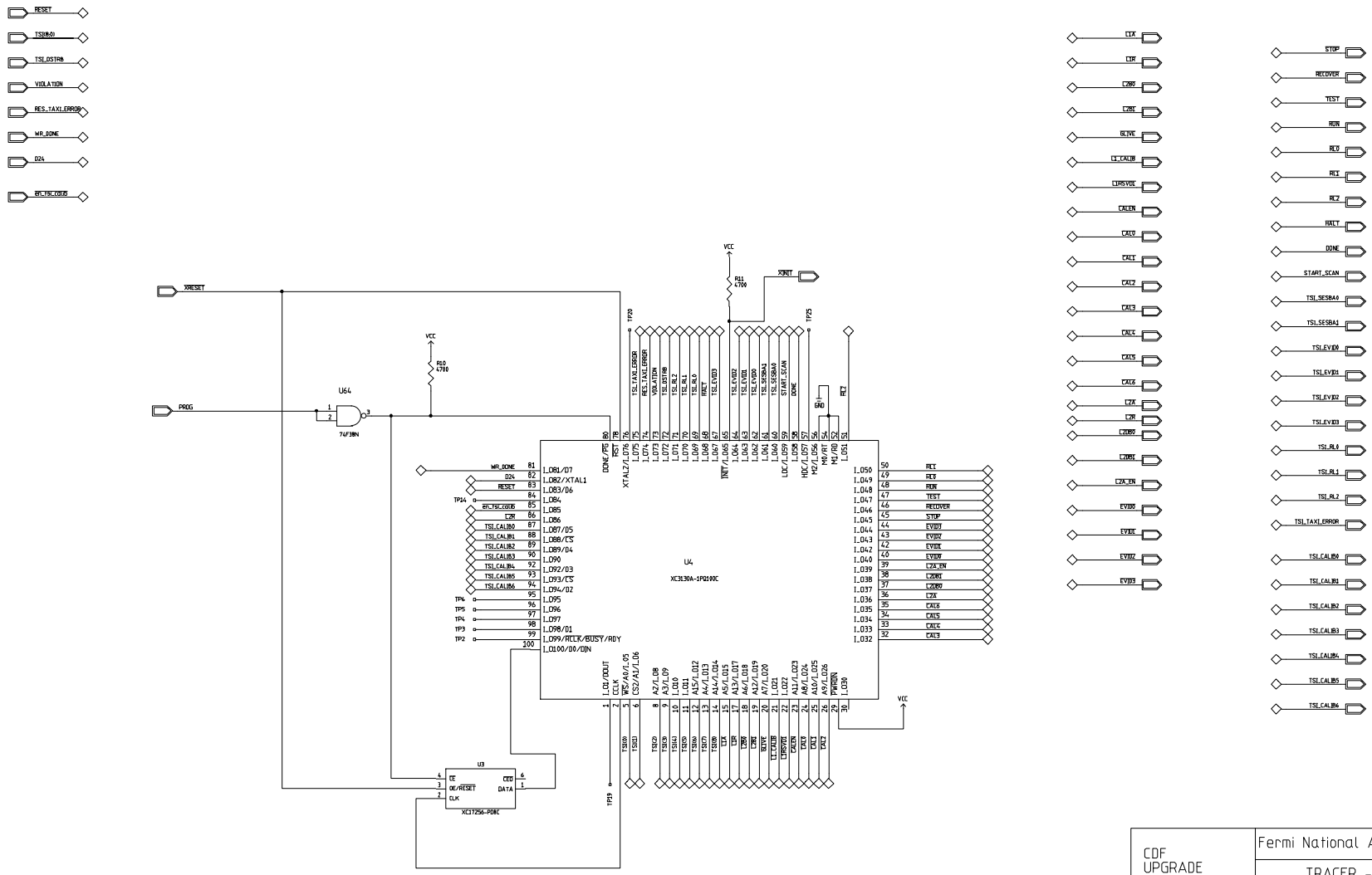
DWG NO	SH	REV

REVISIONS

REV	DESCRIPTION	DATE	APPROVED



CDF UPGRADE ELECTRONICS	Fermi National Accelerator Lab		
	TRACER - Xilinx FPGA		
	TSI Interface Decoding		
Theresa M. Shaw	SIZE D	FSHC NO	DWG NO TMS-TRAC-071197-P9A	REV B
RD/EED/EES	SCALE		SHEET 1 of 1