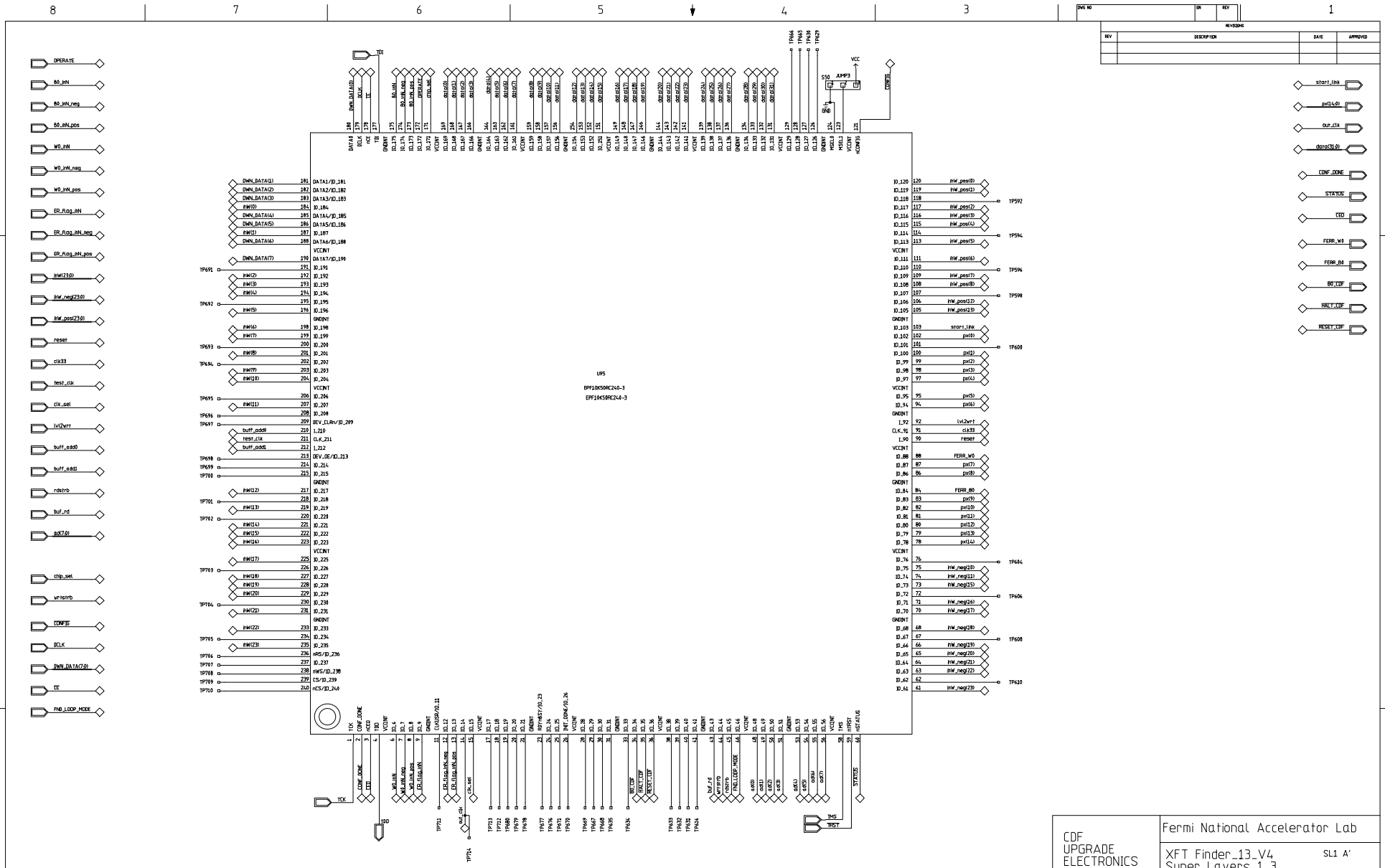

U95
EPF10K50RC240-3
EPF10K50RC240-3
OPERATE
BO_inN
BO_inN_neg
BO_inN_pos
WO_inN
WO_inN_neg
WO_inN_pos
ER_flag_inN
ER_flag_inN_neg
ER_flag_inN_pos
inW(23:0)
inW_neg(23:0)
inW_pos(23:0)
reset
clk33
test_clk
clk_sel
lvl2vrt
buff_add0
buff_add1
rdstrb
buf_rd
ad(7:0)
chip_sel
wrtstrb
CONFIG
DCLK
DWN_DATA(7:0)
CE
FND_LOOP_MODE
start_link
px(14:0)
our_clk
data(31:0)
CONF_DONE
STATUS
CED
FERR_W0
FERR_B0
BO_CDF
HALT_CDF
RESET_CDF
CDF
UPGRADE
ELECTRONICS
Fermi National Accelerator Lab
XFT Finder_13_V4
Super Layers 1 3
FINDER
SL1 A'
Theresa M. Shaw
PPD/ET&T/CDF_Upgrade
TMS-XFTFND13-021999-P9C
A
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