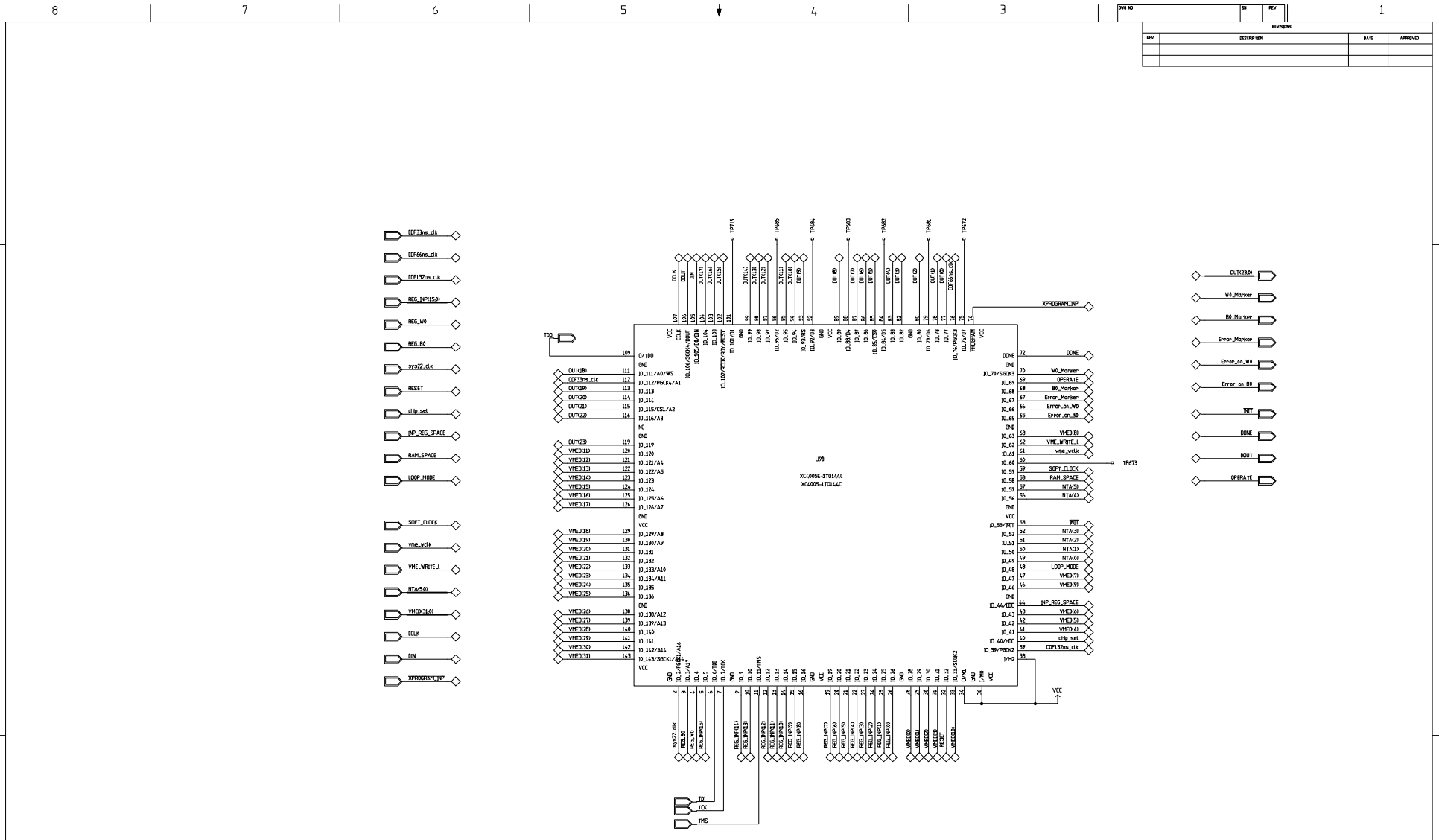

DWG NO	SIM	REV

REVISIONS

REV	DESCRIPTION	DATE	APPROVED

CDF UPGRADE ELECTRONICS

Fermi National Accelerator Lab

XFT Finder_13_V4 SL1 B'
Super Layers 1 3
Alignment Logic - Finder Input

Theresa M. Shaw

PPD/ET&T/CDF_Upgrade

SIZE	FSMC NO	DWG NO	REV
D		TMS-XFTFND13-021999-P7D	A

SCALE | SHEET 1 of 1