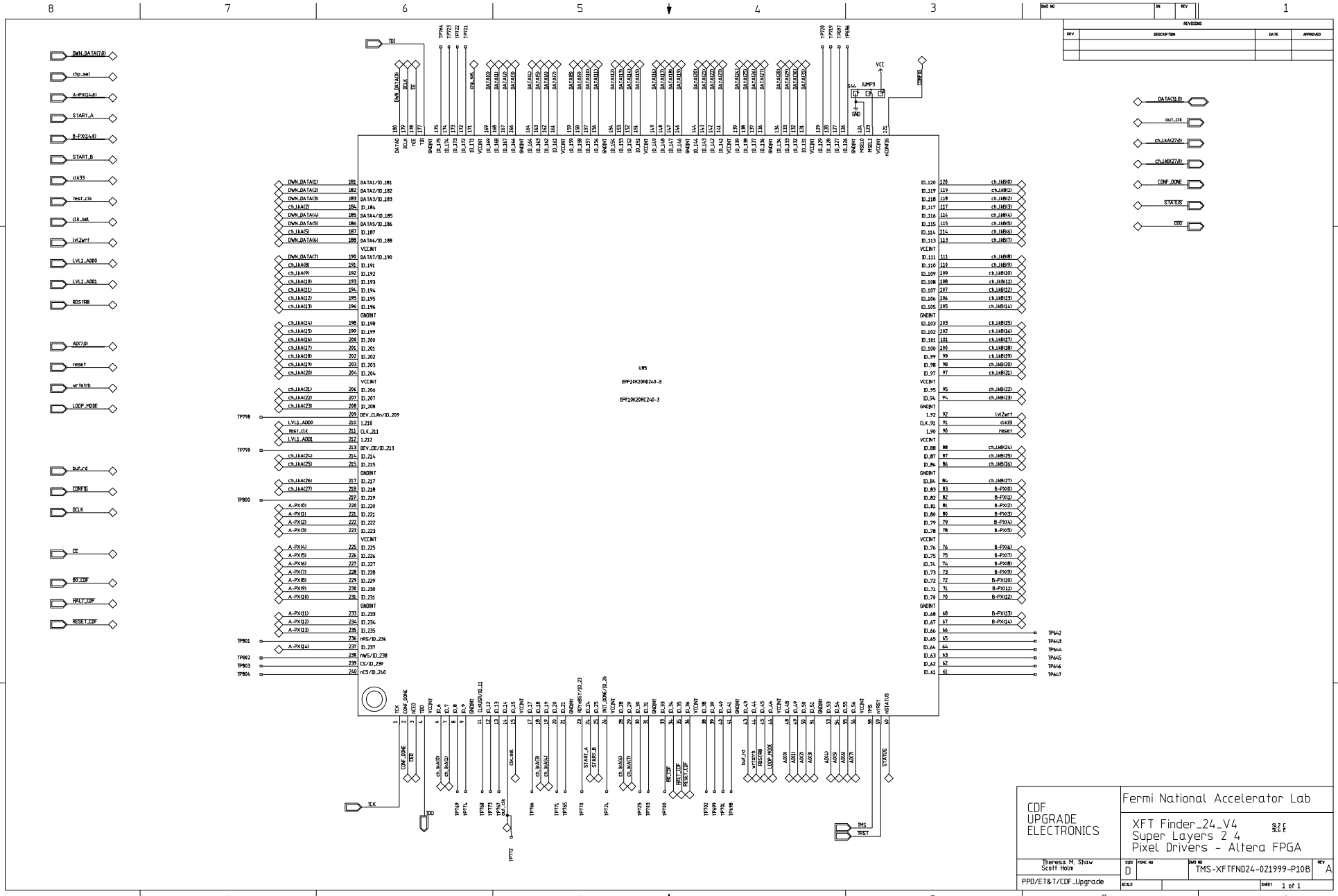
U85
EPF10K20RBD240-3
EPF10K20RC240-3
DWN_DATA(7:0)
ctp_sel
A-PX(14:0)
START_A
B-PX(14:0)
START_B
clk33
test_clk
clk_sel
LV2wrt
LVL1_ADD0
LVL1_ADD1
RDS1RB
ADC(7:0)
reset
wr1strb
LOOP_MODE
buf_rd
CONFIG
DCLK
CE
BO_CDF
HALT_CDF
RESET_CDF
TDI
TCK
TDO
TMS
TRST
VCC
GND
JUMP3
CONFIG
DATA(31:0)
out_clk
ch_lkA(27:0)
ch_lkB(27:0)
CONF_DONE
STATUS
CEO
CDF UPGRADE ELECTRONICS
Fermi National Accelerator Lab
XFT Finder_24_V4
Super Layers 2 4
Pixel Drivers - Altera FPGA
Theresa M. Shaw
Scott Holm
PPD/ET&T/CDF_Upgrade
D
TMS-XFTFND24-021999-P10B
A
1 of 1