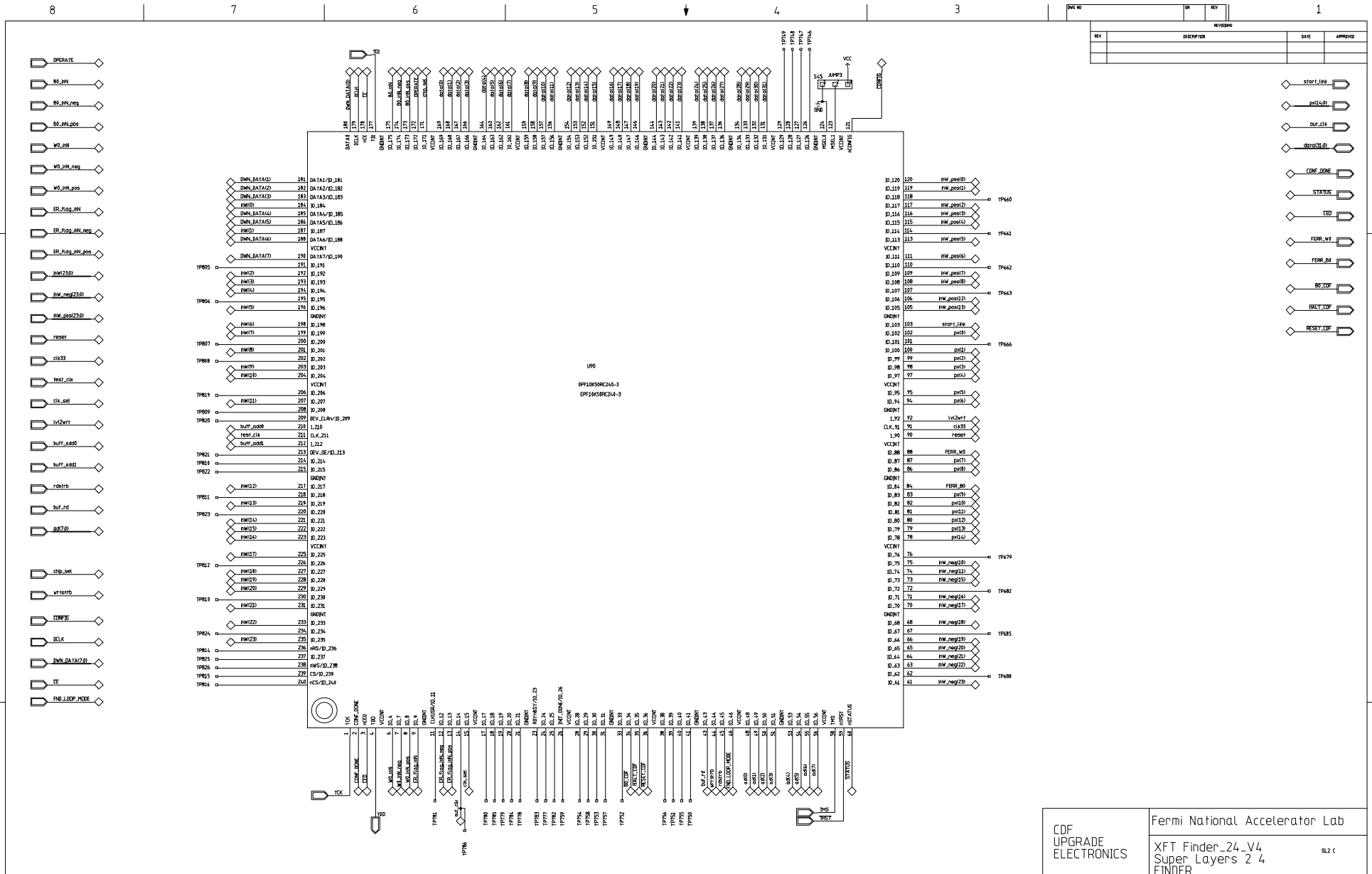
U90
EPF10K50RC240-3
EPF10K50RC240-3
OPERATE
B0_inN
B0_inN_neg
B0_inN_pos
W0_inN
W0_inN_neg
W0_inN_pos
ER_flag_inN
ER_flag_inN_neg
ER_flag_inN_pos
inW(23:0)
inW_neg(23:0)
inW_pos(23:0)
reset
clk33
test_clk
clk_sel
lvl2wrt
buff_add0
buff_add1
rdstrb
buf_rd
ad(7:0)
chip_sel
wrtstrb
CONFIG
BCLK
DWN_DATA(7:0)
CE
FND_LOOP_MODE
start_link
px(14:0)
our_clk
data(31:0)
CONF_DONE
STATUS
CEO
FERR_W0
FERR_B0
B0_CDF
HALT_CDF
RESET_CDF
CDF
UPGRADE
ELECTRONICS
Fermi National Accelerator Lab
XFT Finder_24_V4
Super Layers 2 4
FINDER
Theresa M. Shaw
Scott Holm
PPD/ET&T/CDF_Upgrade
TMS-XFTFND24-021999-P9 C
A
D
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