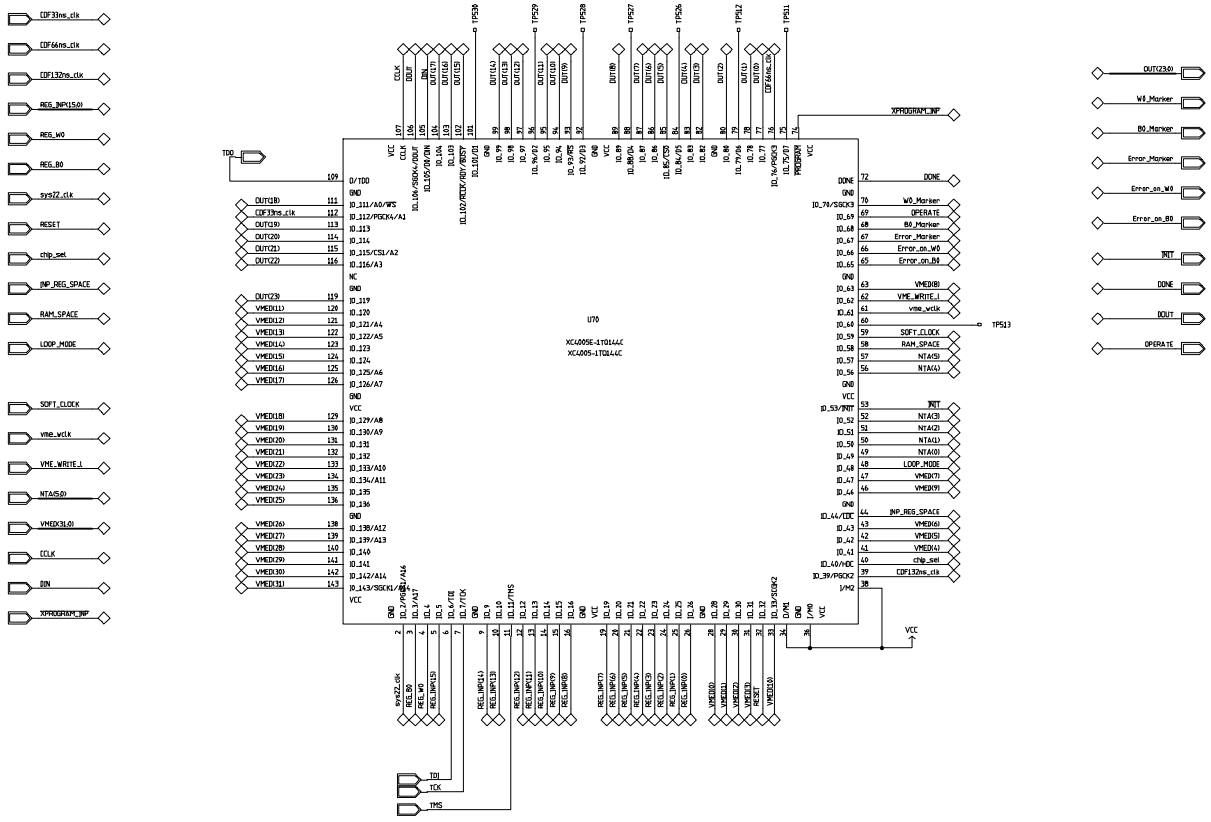
DWG NO	SIN	REV

REVISIONS

REV	DESCRIPTION	DATE	APPROVED



CDF UPGRADE ELECTRONICS	Fermi National Accelerator Lab		
	XFT Finder_24_V4 SL4 -N Super Layers 2 4 Alignment Logic - Finder Input		
Theresa M. Shaw Scott Holm	SIZE D	FSMC NO	DWG NO TMS-XFTFND24-021999-P 8A	REV A
PPD/ET&T/CDF_Upgrade	SCALE		SHEET 1 of 1