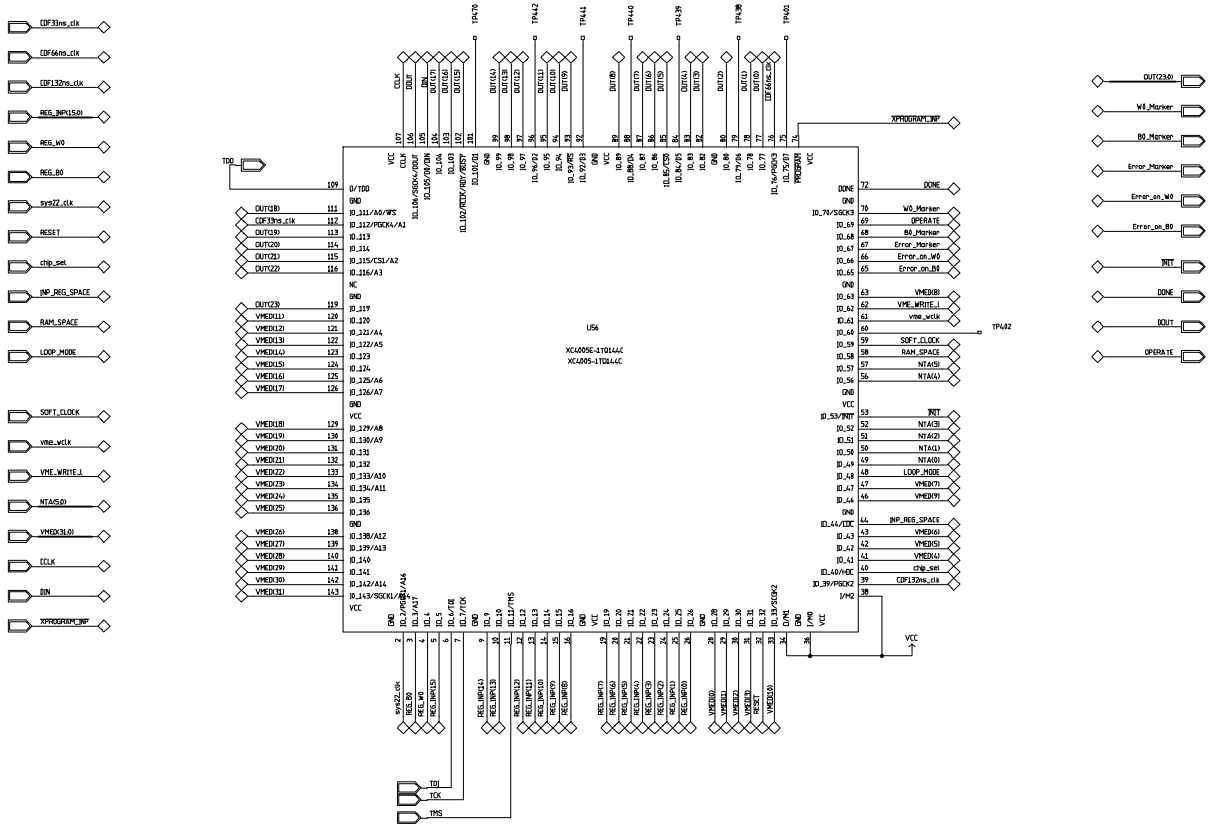
DWG NO	SIN	REV

REVISIONS

REV	DESCRIPTION	DATE	APPROVED



CDF UPGRADE ELECTRONICS	Fermi National Accelerator Lab		
	XFT Finder_24_V4 SL4 A Super Layers 2 4 Alignment Logic - Finder Input		
Theresa M. Shav Scott Holm	SIZE D	DWG NO TMS-XFTFND24-021999-PBB	REV A
PPD/ET&T/CDF_Upgrade	SCALE	SHEET 1 of 1	