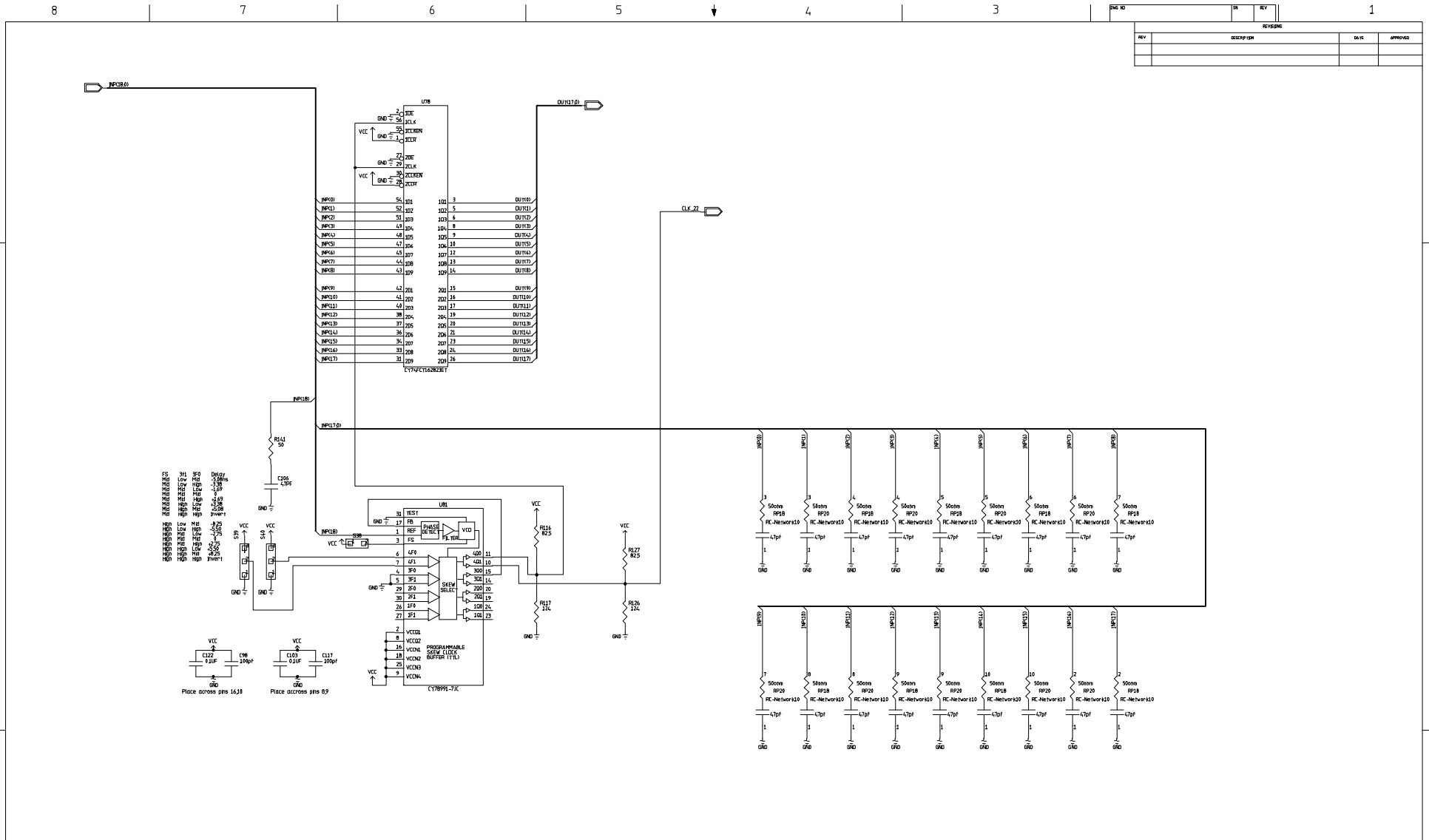


| FS | 3H | 3F0 | Delay |
|---|---|---|---|
| Mid | Low | Mid | -5.08ns |
| Mid | Low | High | -3.38 |
| Mid | Mid | Low | -1.69 |
| Mid | Mid | Mid | 0 |
| Mid | Mid | High | +1.69 |
| Mid | High | Low | +3.38 |
| Mid | High | Mid | +5.08 |
| Mid | High | High | Invert |
| High | Low | Mid | -8.25 |
| High | Low | High | -5.50 |
| High | Mid | Low | -2.75 |
| High | Mid | Mid | 0 |
| High | Mid | High | +2.75 |
| High | High | Low | +5.50 |
| High | High | Mid | +8.25 |
| High | High | High | Invert |

| REV | DESCRIPTION | DATE | APPROVED |
|---|---|---|---|
| | | | |

CDF UPGRADE ELECTRONICS

Fermi National Accelerator Lab

XFT Finder_24_V4
Super Layers 2 4
Input Registers

SL2 B

Theresa M. Shaw
Scott Holm

PPD/ET&T/CDF_Upgrade

SIZE D | DWG NO TMS-XFTFND24-021999-P6 B | REV A

SHEET 1 of 1