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L2 Muon Interface Board Progress Report

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http://www-personal.engin.umich.edu/~mrniell/L2_progress/L2_progress.html

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L2 Muon Board Bugs

- **Programming**
 - **Implementation of JTAG chain is incorrect**
 - **Intermediate fix:**
 - **skip ISP Prom, load firmware on powerup**
 - **Final fix**
 - **little blue wires? still working on it**

MB Controller mezzanine

- **Continued development of MB Interface FPGA firmware**
 - **VME state machine blocks written**
 - **MB state machine blocks written**
 - **Main operation state machine under development**
- **Simple firmware loads**
 - **But only with computer connected to board**
 - **Need to fix implementation of Prom chip**
 - **Flashes front panel led well, but takes 10 minutes to set it up to program**
 - **Working on fixing**

What remains?

- **Firmware, software**
 - **MB controller firmware**
 - **VME panel software (K. Copic)**
- **Debugging**
 - **running in variuos configurations**
- **Ideas:**
 - **Load up muon test crate, insert L2Muon board into test's data path**
 - **software changes minimal**

Time Scale

- **Goals**
 - **Week of August 23**
 - **Finish debugging hardware**
 - **Week of August 30**
 - **Firmware development**
 - **Week of September 6**
 - **Finish VME implementation**
 - **Week of September 13/(20?)**
 - **First B0 tests of L2 Muon Board**

Muon Board

- To explain the programming situation, I have made a convenient diagram:



Niell 8/02