Termination Board Assembly S. Holm 12/22/00

The board may be used to terminate both the CDF_CLK signal and the CDF_TDC_CALIB signal.

1. Front Panel – non silkscreen side of board. Gasket away from board.
2. Stabilizer Bar.
3. P2 Connector – make sure it is flat against the board. Non silkscreen side of board.
4. CDF_TDC_CALIB signal: Surface mount resistors. R3 & R7 = 30 ohm. R6 = 68 ohm.
5. CDF_CLK signal: Surface mount resistor. R8 = 50 ohm.
6. Use example.

Termination Board Installation S. Holm 1/08/01

The board should be installed in the opposite end of the crate from the Tracer module, either in slot **2** or **21**. For SIP removal use a long needle nose pliers and pry under bottom edge of SIP until it pops up, remove with the pliers trying to minimize pin bend. Mark pin 1 on the SIP by scratching on the top of the SIP. Use the custom SIP installation tool: SH001 to install the SIP. After the SIP is started extract the screwdriver from the SH001 and use it to hold the SIP in place while the SH001 is removed. A good light is very useful for seeing pin 1 on the backplane.

If the CDF_CLK signal is being terminated then the following SIPS will need to be removed and modified. Remove N42 and N48, cut pins 6 and 7, tape over the stubs with electrical tape and reinsert into backplane. Make sure that pin 1 is towards the top of the crate.

If CDF_TDC_CALIB signal is being terminated then the following SIPS will need to be removed and modified. N51, cut pins 5 and 6, tape over the stubs and reinsert into backplane. N45 and N39, cut pin 5 on each sip, tape and reinsert into backplane. Make sure that pin 1 is towards the top of the crate.

Use the following links to view the backplane silkscreen and SIP pinout –
http://esdserver1.fnal.gov/~diags/rittal/3687001/docs/general/Schematics/cdf-plot.pdf

http://esdserver1.fnal.gov/~diags/rittal/3687001/docs/general/Schematics/22-of-24.pdf

http://esdserver1.fnal.gov/~diags/rittal/3687001/docs/general/Schematics/16-of-24.pdf