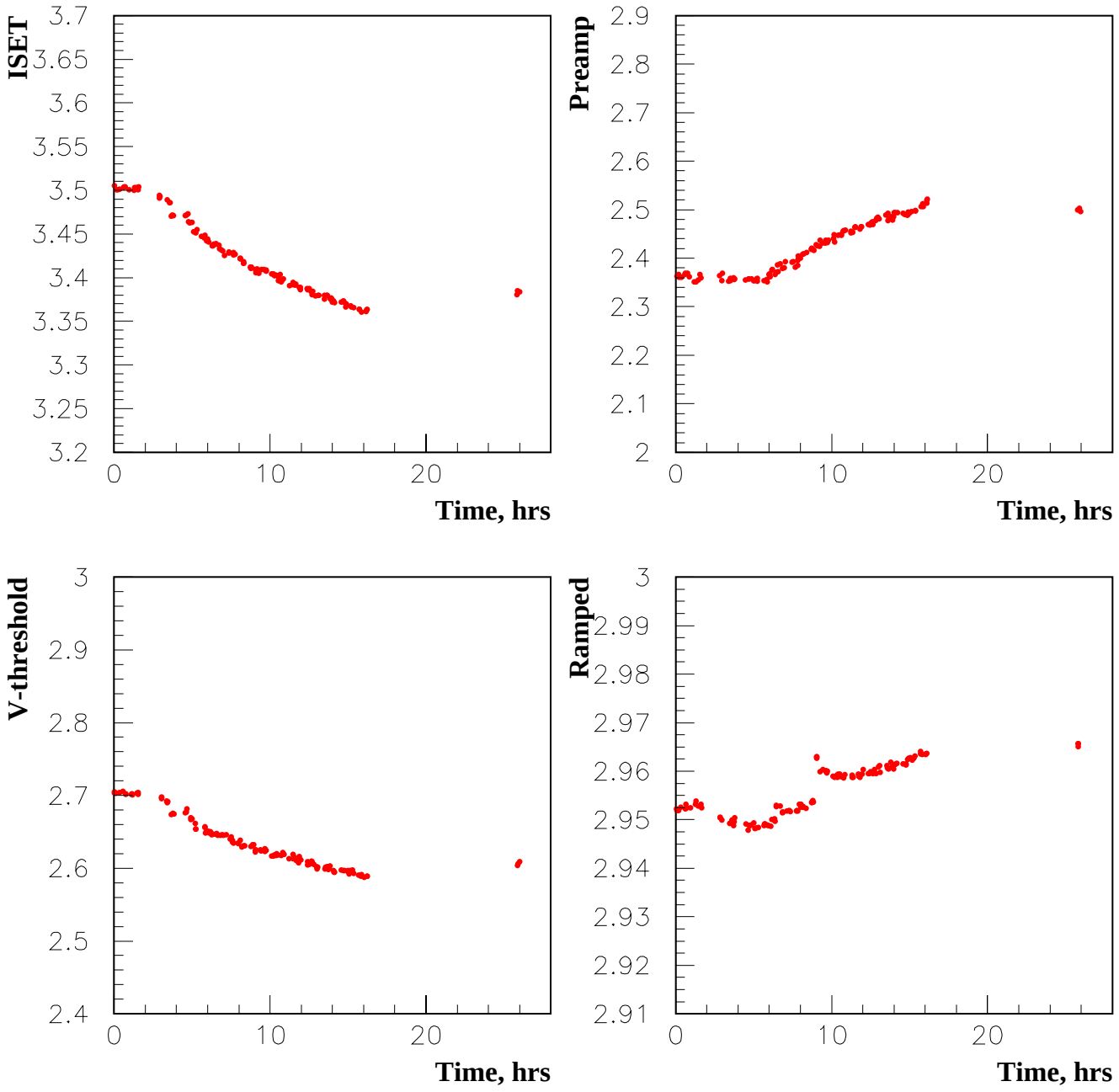SVX-IIId chip bias changes during the run