

L1 Cal Status

- ❑ Mostly smooth operation
- ❑ One problem on Jun 30 with a failed transition board which was replaced with a spare and fixed at the UofC e-shop (blown capacitor).
- ❑ Paged during the last week of shutdown due to a suspicious XTRP connector on a swapped DIRAC board on b011c100.

Before Shutdown

Carla's bad bits list possibly from ADMEM cables before the June2002 shutdown

eta	phi	bit	comment
22	2	em 5	(miscommunication, this meant to be eta=2, phi=22)
11	5	had 0	
16	19	had 0	(both on and off)
21	0	had 1	cable downstairs
22	3	had 4	cable downstairs
22	23	em 7	high order PEM
16 (May)	3	em	bent pins on the backplane, backplane removed and fixed

Pulsed (manually) the AUX-ADMEM cables and in the access checked continuity and termination.

eta	phi		bit	comment
22	2	em	5	OK (ADMEM people checked also)
11	5	had	0	connector/cable: bad pin fixed w/Roberto June 5 afternoon access
16	19	had	0	OK (ADMEM people checked also)
21	0	had	1	connector/cable: very badly bent pin fixed w/Roberto June 5 afternoon access
22	3	had	4	connector/cable very badly recessed pin fixed w/Roberto June 5 afternoon access
22	23	em	7	OK (ADMEM people checked also)

Work on crate B011cl00 during shutdown

- ❑ On slot 4 the J12 backplane connector seemed to have wiggled/misaligned pins. Roberto fixed .
- ❑ Dirac 113 swapped with 079 on slot 4- BC error is gone.
- ❑ Dirac 16 swapped with 96 (slots 21 with 20) for cabletests (june 14) and back (June19) due to a possible XTRP connector problem on DIRAC 96 (every time there was a swap a CDF_ERROR was pulled and had to take both boards out of the run and back one by one to eliminate the error—weird, log at day shift June 19)
- ❑ Problems (BC errors, CDF errors) emanated from this crate before and during the shutdown; keep an eye **(** Carla reports no problems since the shutdown on this crate**)**

Remaining to be fixed in the next access

eta	phi		bit	(problem)
2	22	em	5	(as before— but we did not look b/c of mistaken eta-phi)
2	5	em	7	(found after the shutdown)
11	5	had	0	(as before immediately after shutdown (june26) presently eliminated)
16	19	had	0	(as before)
21	0	had	1	(as before immediately after the shutdown (june26), presently eliminated)

Could not get time in the last access to look at [2,22] and [2,5];
Will need to do that during the next access. [16,19] is probably
not a cable problem?

Cabletests

Arnd kludged on RC Heather/Monica cabletest code which runs with a specific table- the run config is under PRIVATE-MEYER-CABLETEST). The ADMEM diagnostic FIFO is loaded

with 0x0000

0x0002

0x0004

0x0008

0x0010

...

0x1000

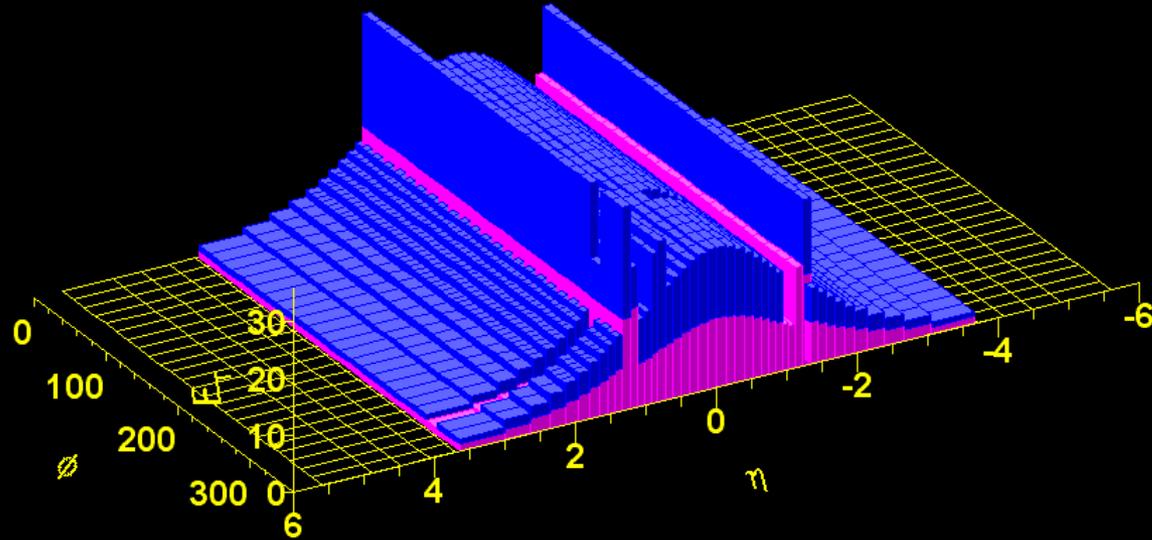
with 1000 events for each setting

Force a Halt-Recover-Run sequence after each reloading in the middle of the run.

In the process found that three ADMEMS have a broken Diagnostic FIFO– (**request to replace in next access**)

Cabletest EVD

Event : 12003 Run : 146552 EventType : DATA | Unpresc: unknown Presc: unknown Myron mode: 0



The west wall crates were not downloaded correctly but Arnd fixed.

Other

- ❑ TRIGSIM/TRIGMON: no modifications
- ❑ Software/Firmware Changes: ongoing on PreFred for timing
- ❑ L1 tstand at UofC under way
(software issues with default (v2_0) cdfvme_common
/ we used to have v1_1)
- ❑ All DIRACs are synced between CDF Module and Hardware
database; all spares entered (how many spares we need here?)
- ❑ Most other L1 boards are also synced