

Proposed Bits

1. CMU lo
2. CMX lo
3. Two CMU lo
4. Two CMU lo, oppq, non-BB
5. CMU-CMX lo, oppq, non-BB
6. Two CMX lo, oppq, non-BB
7. CMU med
8. CMUP med
9. CMUP hi
10. CMU med track (no stub)
11. CMX med
12. Two CMX med
13. CMX hi
14. CMUP+CMUstub, non-BB
15. CMUP+CMXstub, non-BB
16. CMU eta gap (hi)
17. CMP phi gap
18. Monopole
19. BMU front (w/track)
20. BMU rear (w/TSU)

Notes

- Nominal Thresholds
 - CMU low: 1.5 GeV
 - CMX low: 2.0 GeV
 - Medium: 4.0 GeV
 - High: 8.0 GeV
- Not Back-to-back cut means to exclude matchbox opposite and its neighbors
 - For CMUP+CMU low stub, also exclude same matchbox
- Which matchbox for 4 GeV CMUP on extra outputs to be used for correlations in L2

Features

- Rate reduction in dimuons
- Phi and Eta gap triggers
 - 20% more top that we planned for, but have not yet included
- CMU and CMX 4 GeV for added SUSY dilepton acceptance
- Restore backup trigger for muon and XFT efficiency study
 - CMUP6_PT4 now has rate limit
 - Problem gets worse at high luminosity
- Bunch 0 trigger will require special configuration
 - Put on extra MTSC output cable