

TEST-STANDS STATUS

Function of test-stands:

To provide permanent (for the duration of Run II) infrastructure for FE/trigger subsystems to debug problems and commissioning boards.

Where:

Most test-stands are located on 2nd floor B0 in room 202 and 203.

Basic setup:

- VME crate with cutout side-panel for scope/analyzer access,
 - Most crates have the same power supply as the real system,
 - CDF master clock connection,
 - Dual CDF DAQ TS and dedicated TS connections.
- (no VRB connection is planned)

CALORIMETER TEST-STAND (Room 202)



User: ADMEM, SMXR, TOF,
Hadron ASD.

Setup: CDF VME crate, 6U SMD,
NIM crate, dark box for PMT
testing.

Status: The setup is basically complete.
The only missing piece is clock and
TS connection.

CDF CLOCK/TS RACK (Room 202)

CDF Clock Crate



Function: provide CDF clock and dedicated TS capability to test-stands.

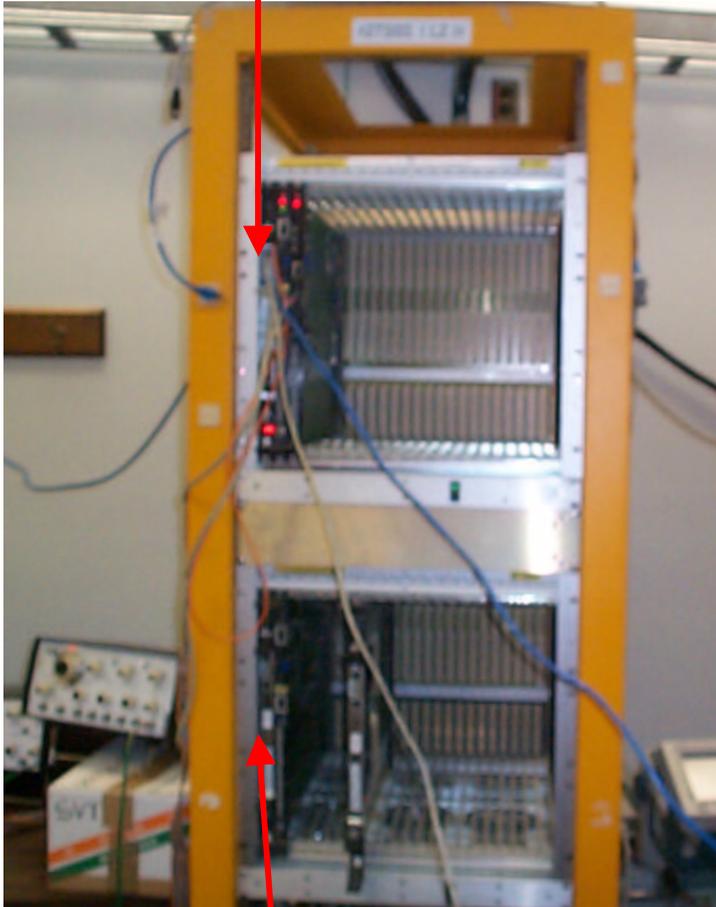
Clock Crate: currently has 6 fanout ports. Will added 2 more fanout ports and the pseudo BC signal.

TS crate: 4-slot TDC and 4-slot calorimeter backplanes are being mounted on the VME crate. All the TS cards required for the setup are ready to be plugged in. (Crate may also be used for FRED card testing in the future)

Dedicated TS crate will be mounted here

L2 TEST-STAND (Room 202)

(TSTL2TRG1.FNAL.GOV)



(TSTL2TRG2.FNAL.GOV)

User: PULSAR, UCLA and Michigan folks.

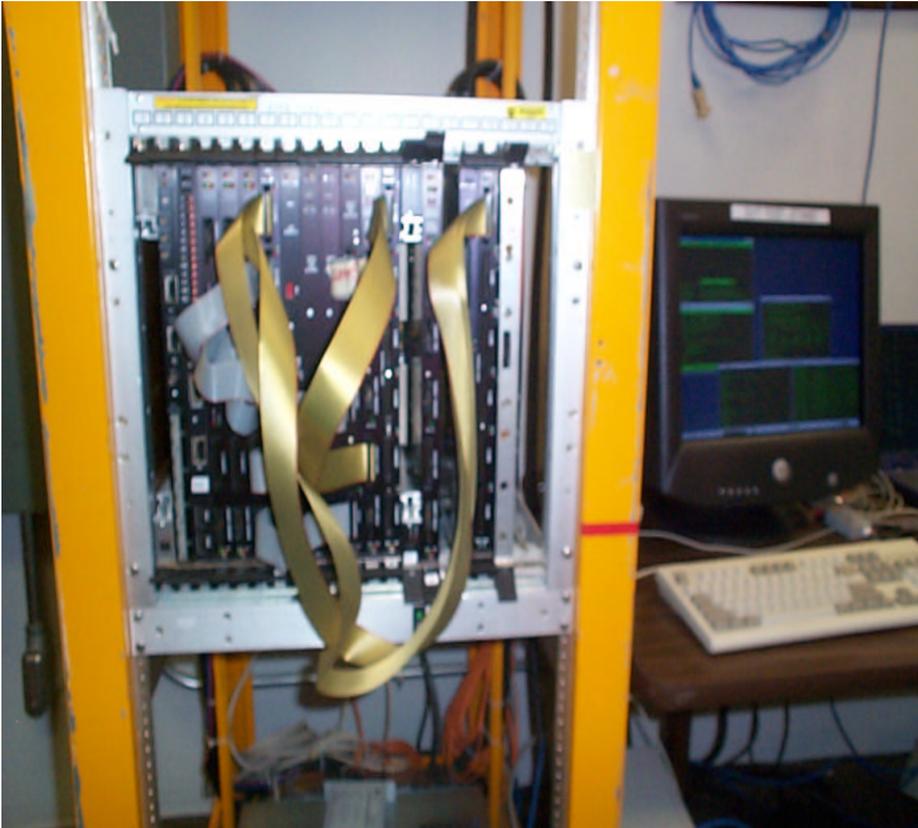
Top crate: fitted with calorimeter J3 backplane.

The crate is being used primarily by the PULSAR folks to commission the board. The crate is missing TS connection.

Bottom crate: fitted with new magicbus backplane.

The crate is intended to be shared among L2 folks. The crate is missing CDF clock, TS and Ethernet connections.

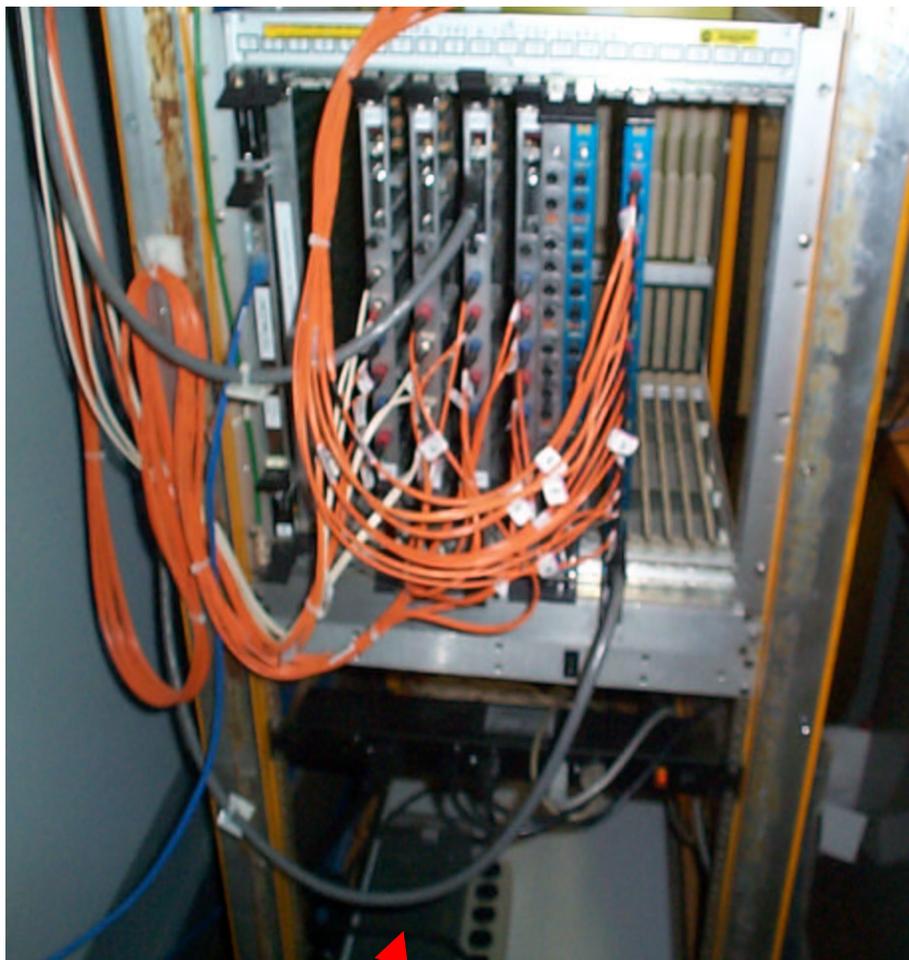
SVT TEST-STAND (Room 202)



User: mainly Bill Ashmanskas for SVT work and occasionally the PULSAR folks.

Status: the setup is essentially complete and has been actively used by Bill. The missing piece is the clock/TS connection.

L1 MUON TEST-STAND (Room 203)



3.3V PS

Serves: nominally L1 muon board, however, Eric James use it extensively to check out TDCs.

Setup: CDF VME crate with TDC J3 backplane. A 3.3V power supply was recently added for the L2 muon board commissioning.

Missing piece is clock/TS connection.

XFT TEST-STAND (Room 203)



Serves: XFT finder boards, XFT linker boards, and XTC mezzanine cards.

Setup: top crate with XFT backplane, bottom crate with split TDC and calorimeter backplanes.

Missing 1 Ethernet and clock/TS connections.

OTHER TEST-STANDS/CRATES

Power supply test-stands
(middle clean room)



CAEN PS

- Muon ASD test-stand (room 203)
- DAQ/TDC test-crate(trigger room, 2RR22C-T)
- L2 decision crate (trigger room, 2RR30C)
- COT test-stand (west clean room)
- XFT SL7 test-crate (counting room, 1RR27F-B)

OUTSTANDING ISSUES

CDF Clock Signal (minor work):

- Need to get one additional fanout module from Steve Chappa,
- Need to connect pseudo BC signal,
- VME backplane clock termination needs to be modified on some of the crates (calorimeter, SVT, L1 muon, and TS crates).

Trigger Supervisor Connection:

- TS fibers and patch panel have arrived at FNAL,
- Cable trays to house the fibers have been installed in room 202,
- Purchase order for the RXPT cable (Belden 1133A, 2000ft) just got sent out, arrival date is ??? (bottleneck),
- Trigger XPT spigots need to be assigned and cables labeled,
- Updating the database for the CDF DAQ TS connection is trivial. However, getting the stand-alone TS running may require significant changes in Run Control (per Bill Badgett).

We are almost done !!!!