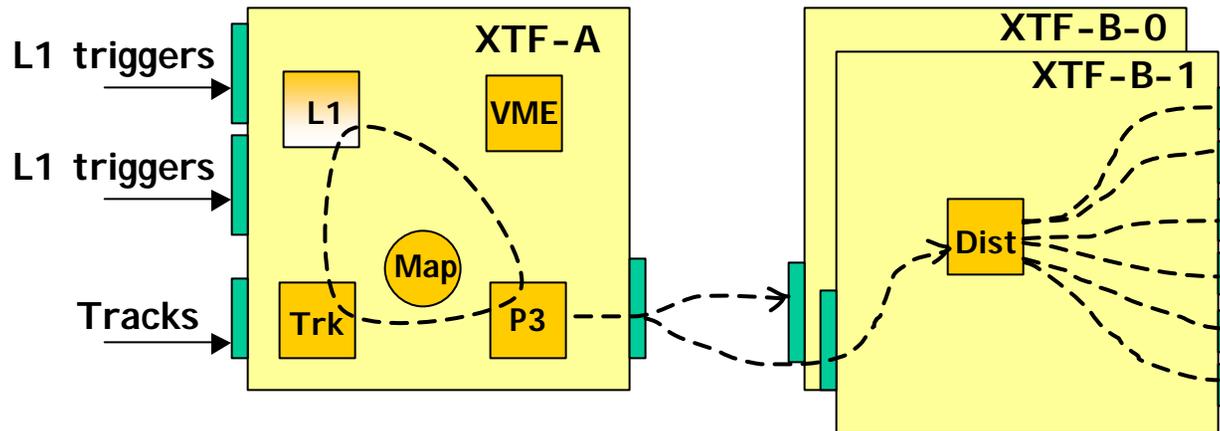


XTF-A/XTF-B

- Today we have in the SVT test stand at B0
 - 1 XTF-A V1.0
 - 2 XTF-B V1.1
 - 1 Interface board (to simulate the L1 bits datastream)
- Today we know
 - Integrated XTF-A/XTF-B test successful
 - L1 trigger block test successful



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- Minor problems in XTF-A pcb
 - Precidip do not fit EPROM and Oscillator holes
 - Stiffener screws do not fit the holes: solved with by Franco with some help from INFN technicians
- Plans for tests & software development
 - Receive data from XTRP (done with a bunch of words)
 - Receive data from L1 Global Trigger
 - Write low level routines in Stefano's style
 - Write ColdStart routine
 - Write test procedures for Franco's GUI
- Plans for production
 - 2 XTF-A V1.0
 - 2 XTF-B V1.1
 - Boards can be assembled as soon as we are happy with the tests (Carlo Magazzu' interacting with Verrieri)
- Front Panels for XTF-A/XTF-B built (need to check if they fit on boards)

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