

CDF Clock System Status

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A. **Clock System Installation**

- Presently, all Clock signal outputs from the Clock LVDS cables are timed to within 1 ns of each other. This timing was done in late June.
- The Clock crate is in the final configuration. However, the Sequencer is still the original Dzero version. The revised module should be ready for the detector's engineering run.
- The Clock rack is close to completion. I still need to install the final power supply and some monitoring equipment.
- The pickup signals, as well as the Tevatron's RF and TVBS signals, are now at the Clock rack. Attempt at synchronization can now commence.

B. **Progress during the Accelerator's Engineering Run**

- So far, the accelerator has been running only un-coalesced beam during the stores. They have been doing a lot of tuning studies. Until this week, there was uncertainty about the identity of the P1 location.
- With the P1 location now set, a one-bunch proton store can now be attempted. This attempt is scheduled for Friday starting around 11:00 AM and should last a couple of hours. This will provide an opportunity to verify the absolute timing and determine my program offsets. Hopefully during this store, I can attempt a first try at synchronization for the luminosity people.
- Hopefully, another P1-only store can then be made sometime next week to complete the synchronization procedure.
- They (the Operations People) may try to setup for collisions prior to roll-in (scheduled for Sep. 5th). This is provided that everything from now to shut-down runs perfectly.

C. **Proposed Synchronization Procedure**

- See timing diagram.