



Trigger Operations Organization

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P. Wilson
Trigger Workshop
8/17/00

-
- We have significant challenges ahead to commission the Trigger systems – will need to work together to complete this task in a timely fashion
 - Propose 24/7 Trigger Shifts during the commissioning run (and beginning of Run II)
 - Start when door closes (around Sept 20?)
 - Present in Control room/Trigger room during beam data
 - On call if no beam?



Trigger Operations Organization

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- For debugging coverage during the run will need more experts available
 - Will require cross training of Trigger experts between sub-systems where
 - Need documentation of systems and tools to train other experts and Trigger shift takers
 - Example arrangement with 5 pools of experts:
 - L1CAL/L1Decision
 - XFT
 - XTRP/L1 Muon
 - L2 Cal/L2 Decision
 - SVT



Support for Trigger Operations

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- Test Equipment (some shared with detector groups):
 - 3 HP Logic analyzers – more available from WH
 - 1 Tek logic analyzer
 - 2 HP Infinium 500 MHz scopes – more available from WH
 - 4 Tek “LunchBox” 500 MHz scopes
 - Misc older/slower scopes
 - Setting up system with test equipment manager
 - Do we need more scopes/LA?



Support for Trigger Operations

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- Storage of small tools and parts is available in small locked cabinets:
 - XFT (in use)
 - SVT (in use)
 - L1 Cal/L1 Decision
 - TSI/FRED
 - L2 Cal/L2 Decision
 - Muon
 - XTRP



Support for Trigger Operations

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- Storage of VIPA cards is now available across from PUMP current.
 - Old 9U VME crates mounted in relay racks with a locking door (just need to get locks)
 - Card guide spacing modified to 1" for easier storage
 - Current capacity is about 150 modules
- Teststands in 2nd floor tech area:
 - First teststand (Muon Trigger) has been operational for several months
 - Expect to rearrange room for additional teststand in September



B0 Maintenance Test-stands

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- ✓ Trigger Type 1
 - DIRAC, Cratesum, PreFRED, FRED, Muon Match, DCAS, LOCOS
 - 220 pin J3
 - Muon Trigger teststand
- ✓ Trigger Type 2: Level 2 Processor
 - L2 Processor and interface boards
 - Magic Bus J3
 - Located in Trigger room
- Trigger Crate Type 3:
 - All SVT, Done XPT
 - SVT J3
- ✓ Calorimeter Frontend Crate with 6U SMD crate
 - Service CMU ASD, HAD ASD, ADMEM, SMXR, and CALIB
 - 160pin feed-through J3
 - Currently in middle clean room
- ✓ TDC + TSI crate
 - Muon and COT transition and mezzanine cards, TS and XPT
 - 220pin feed-through J3
 - TDC teststand in West clean room
- Serviced elsewhere:
 - PREP: TDCs, FIB, VRB, MVME
 - WH14: XFT, TRACER, 6U-9U Adapter
 - Illinois: XTRP