

Incomplete DAQ Tasks

4/28/2005

% Complete	WBS #	Task Name	Start	Finish	Total Cost(FY02, no overhead)	M&S Cost	M&S Cont Factor	M&S Contingency	FNAL Labor Cost	Labor Cont. Factor	Labor Contingency	Remaining Contingency (with Overhead)
	<b>1.3</b>	<b>Run 2b DAQ and Trigger Project</b>			\$2,516,960			\$962,753	\$397,734		\$286,926	\$930,836
	<b>1.3.1</b>	<b>Run 2b TDC Project</b>										
	<b>1.3.1.12</b>	<b>TDC Modifications</b>										
	<b>1.3.1.12.2</b>	<b>Modification of Old TDC's</b>										\$71,504
95%	1.3.1.12.2.2	Modify Boards 51 through 100	03/15/05	04/25/05	\$16,050	\$450	0.3	\$135	\$15,600	0.5	\$7,800	\$568
0%	1.3.1.12.2.3	Modify Boards 101 through 150	04/26/05	05/23/05	\$16,050	\$450	0.3	\$135	\$15,600	0.5	\$7,800	\$11,356
0%	1.3.1.12.2.4	Modify Boards 151 through 200	06/01/05	06/28/05	\$16,050	\$450	0.3	\$135	\$15,600	0.5	\$7,800	\$11,356
0%	1.3.1.12.2.5	Modify Boards 201 through 250	07/07/05	08/03/05	\$16,050	\$450	0.3	\$135	\$15,600	0.5	\$7,800	\$11,356
0%	1.3.1.12.2.6	Modify Boards 251 through 300	08/11/05	09/08/05	\$16,050	\$450	0.3	\$135	\$15,600	0.5	\$7,800	\$11,356
10%	1.3.1.12.2.7	Purchase components	01/10/05	08/03/05	\$10,000	\$10,000	0.3	\$3,000	\$0	0	\$0	\$3,447
	<b>1.3.1.12.3</b>	<b>Test and Calibrate TDC's at Michigan</b>										
30%	1.3.1.12.3.2	Test Boards 51 through 100	03/21/05	04/15/05	\$5,450	\$5,450	0.3	\$1,635	\$0	0	\$0	\$1,461
0%	1.3.1.12.3.3	Test Boards 101 through 150	05/03/05	05/31/05	\$5,450	\$5,450	0.3	\$1,635	\$0	0	\$0	\$2,087
0%	1.3.1.12.3.4	Test Boards 151 through 200	06/08/05	07/06/05	\$5,450	\$5,450	0.3	\$1,635	\$0	0	\$0	\$2,087
0%	1.3.1.12.3.5	Test Boards 201 through 250	07/14/05	08/11/05	\$5,450	\$5,450	0.3	\$1,635	\$0	0	\$0	\$2,087
0%	1.3.1.12.3.6	Test Boards 251 through 300	08/18/05	09/15/05	\$5,450	\$5,450	0.3	\$1,635	\$0	0	\$0	\$2,087
0%	1.3.1.12.3.7	Purchase replacement components	01/18/05	08/10/05	\$2,000	\$2,000	0.3	\$600	\$0	0	\$0	\$766
	<b>1.3.1.12.7</b>	<b>New Calibration Test Stand Equipment</b>										
0%	1.3.1.12.7.2	Card fabrication and assembly	04/04/05	05/27/05	\$12,000	\$12,000	0.3	\$3,600	\$0	0	\$0	\$4,596
0%	1.3.1.12.7.3	Software and firmware development	04/04/05	07/26/05	\$18,000	\$18,000	0.3	\$5,400	\$0	0	\$0	\$6,893
	<b>1.3.2</b>	<b>Run 2b Level 2 Project</b>										\$48,780
33%	1.3.2.13	Firmware development for deployment in B0	01/05/05	09/30/05	\$49,896	\$0	0	\$0	\$49,896	0.75	\$24,948	\$35,951
0%	1.3.2.14	L2 Filar Hardware purchase	05/16/05	08/09/05	\$33,500	\$33,500	0.3	\$10,050	\$0	0	\$0	\$12,829
	<b>1.3.11</b>	<b>Revised XFTII Project</b>						\$311,838			\$88,059	\$488,839
	<b>1.3.11.2</b>	<b>Finder Boards</b>										
75%	1.3.11.2.2	Finder Board FPGA Firmware development	06/15/04	06/27/05	\$45,760	\$0	0	\$0	\$45,760	1	\$22,880	\$16,403
	<b>1.3.11.2.4</b>	<b>Preproduction Finder boards (FNAL)</b>										
50%	1.3.11.2.4.9	Test stand software	09/24/04	09/29/05	\$5,264	\$0	0	\$0	\$5,264	1	\$2,632	\$3,774
50%	1.3.11.2.4.10	Finder Board Preproduction Testing	02/21/05	05/13/05	\$47,040	\$0	0.5	\$0	\$47,040	1	\$23,520	\$33,724
25%	1.3.11.2.4.12	Finder Board Preproduction Modification	03/07/05	05/13/05	\$22,700	\$0	0	\$0	\$22,700	1	\$11,350	\$24,411
	<b>1.3.11.2.5</b>	<b>Production Finder Boards</b>										
0%	1.3.11.2.5.2	Prepare bid package	03/22/05	05/16/05	\$7,520	\$0	0	\$0	\$7,520	0.5	\$3,760	\$5,391
5%	1.3.11.2.5.3	Purchase Production Finder Board componen	03/22/05	06/14/05	\$440,936	\$440,000	0.3	\$132,000	\$936	0.5	\$468	\$160,717
0%	1.3.11.2.5.14	Test first 5 production Finder boards	07/07/05	08/03/05	\$8,160	\$0	0	\$0	\$8,160	0.5	\$4,080	\$5,850
0%	1.3.11.2.5.6	Checkout Production Finder Boards	09/09/05	10/27/05	\$15,330	\$0	0	\$0	\$15,330	0.5	\$7,665	\$10,990
0%	1.3.11.2.5.11	Final revision of test stand software	05/16/05	09/07/05	\$3,008	\$0	0	\$0	\$3,008	1	\$1,504	\$4,313
50%	1.3.11.3	Test equipment	01/14/05	08/16/05	\$23,773	\$23,773	0.3	\$7,132	\$0	0	\$0	\$4,552
	<b>1.3.11.4</b>	<b>TDC Transition Module</b>										
93%	1.3.11.4.3	Preproduction TDC Fiber Transition Boards	06/29/04	04/18/05	\$10,000	\$10,000	0	\$0	\$0	0	\$0	\$0
	<b>1.3.11.4.4</b>	<b>Production TDC Fiber Transition Boards</b>										
20%	1.3.11.4.4.2	Purchase TDC Transition Board components	02/21/05	04/29/05	\$160,000	\$160,000	0.5	\$80,000	\$0	0.5	\$0	\$81,699
	<b>1.3.11.5</b>	<b>TDC Mezzanine Card</b>										
75%	1.3.11.5.1	TDC Mezzanine Card firmware development	06/01/04	07/12/05	\$61,600	\$61,600	0.3	\$18,480	\$0	0	\$0	\$5,898
	<b>1.3.11.5.3</b>	<b>Production TDC Mezzanine Card</b>										

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0%	1.3.11.5.3.5	Checkout of Production TDC Mezzanine Card	04/06/05	06/15/05	\$5,500	\$5,500	0.5	\$2,750	\$0	0.5	\$0	\$3,511
	<b>1.3.11.6</b>	<b>Cables</b>										
0%	1.3.11.6.1	Finder 3D to L2 Stereo Pulsar	03/01/05	05/09/05	\$4,000	\$4,000	0.3	\$1,200	\$0	0.5	\$0	\$1,532
0%	1.3.11.6.2	Finder to SLAM	03/01/05	05/09/05	\$12,480	\$12,480	0.3	\$3,744	\$0	0	\$0	\$4,779
	<b>1.3.11.6.3</b>	<b>TDC to Finder</b>										
30%	1.3.11.6.3.2	Order TDC to Finder cables	02/23/05	05/03/05	\$35,000	\$35,000	0.3	\$10,500	\$0	0	\$0	\$9,383
0%	1.3.11.6.3.3	Install TDC to Finder cables	05/04/05	06/01/05	\$6,240	\$0	0	\$0	\$6,240	0.5	\$3,120	\$4,474
	<b>1.3.11.7</b>	<b>Stereo Linker Association Module (SLAM)</b>										
65%	1.3.11.7.3	Linker Board FPGA Firmware modifications	07/28/04	08/09/05	\$20,280	\$20,280	0.3	\$6,084	\$0	0	\$0	\$2,718
	<b>1.3.11.7.4</b>	<b>Preproduction SLAM Boards</b>										
50%	1.3.11.7.4.7	SLAM Board Preproduction testing	01/24/05	04/01/05	\$7,800	\$7,800	0.3	\$2,340	\$0	0	\$0	\$1,494
0%	1.3.11.7.4.9	SLAM Board Preproduction modification	04/19/05	05/02/05	\$1,560	\$1,560	0.3	\$468	\$0	0	\$0	\$597
	<b>1.3.11.7.5</b>	<b>Production SLAM Boards</b>										
0%	1.3.11.7.5.2	Purchase SLAM Board components	03/21/05	05/27/05	\$121,560	\$121,560	0.5	\$36,468	\$0	0	\$0	\$77,589
0%	1.3.11.7.5.5	Checkout Production SLAM Board	06/23/05	09/01/05	\$6,240	\$6,240	0.3	\$1,872	\$0	0	\$0	\$2,390
	<b>1.3.11.8</b>	<b>Level 2 Stereo Interface</b>										
0%	1.3.11.8.2	Level 2 Interface Board Firmware (ILL)	02/08/05	10/20/05	\$17,600	\$17,600	0.5	\$8,800	\$0	0.5	\$0	\$11,234
25%	1.3.11.8.3	Level 2 Interface Board Firmware (FNAL)	01/18/05	09/29/05	\$7,920	\$0	0.5	\$0	\$7,920	0.5	\$3,960	\$4,259
	<b>1.3.11.8.5</b>	<b>Pulsar &amp; Mezzanine boards</b>										
20%	1.3.11.8.5.7	Checkout of Pulsar boards for Level 2 stereo	03/14/05	05/06/05	\$6,240	\$0	0	\$0	\$6,240	1	\$3,120	\$7,158
	<b>1.3.4</b>	<b>Event-Builder Upgrade</b>										\$19,350
	<b>1.3.4.3</b>	<b>upgrade software</b>										
57%	1.3.4.3.6	Software commissioning	12/01/04	06/29/05	\$130,000	\$130,000	0	\$39,000	\$0	0	\$0	\$0
	<b>1.3.4.5</b>	<b>construct full size system</b>										
	<b>1.3.4.5.4</b>	<b>purchase remaining hardware</b>										
25%	1.3.4.5.4.7	Purchase additional VRB Crate hardware	02/01/05	07/20/05	\$40,000	\$40,000	0.3	\$12,000	\$0	0	\$0	\$11,489
11%	1.3.4.11	Engineering support for testing	03/14/05	09/28/05	\$12,320	\$0	0	\$0	\$12,320	0.5	\$6,160	\$7,861
	<b>1.3.5</b>	<b>Computer for Level3 PC Farm / DAQ</b>										\$234,980
	<b>1.3.5.4</b>	<b>replace 20 DAQ PCs (2005)</b>										
0%	1.3.5.4.2	purchase formalities	05/06/05	08/01/05	\$91,781	\$91,781	0.1	\$27,534	\$0	0	\$0	\$11,716
	<b>1.3.5.5</b>	<b>replace 70 Level 3 PCs (2004)</b>										
0%	1.3.5.5.8	Contingency cost task for L3 cost overrun	05/02/05	05/06/05	\$61,576	\$61,576	0	\$0	\$0	0	\$0	\$0
	<b>1.3.5.6</b>	<b>replace 140 Level 3 PCs (2005)</b>										
0%	1.3.5.6.2	purchase formalities	05/06/05	08/15/05	\$499,704	\$499,704	0.3	\$149,911	\$0	0	\$0	\$191,369
	<b>1.3.5.9</b>	<b>Purchase Converter nodes</b>										
0%	1.3.5.9.1	purchase 28 Level 3 converter nodes	05/06/05	08/15/05	\$83,284	\$83,284	0.3	\$24,985	\$0	0	\$0	\$31,895
	<b>1.3.6</b>	<b>SVT upgrade</b>										\$67,383
	<b>1.3.6.1</b>	<b>Design and Simulation</b>										
	<b>1.3.6.1.2</b>	<b>Hit Buffer</b>										
20%	1.3.6.1.2.3	Firmware	01/07/05	09/28/05	\$81,400	\$0	0	\$0	\$81,400	0.5	\$40,700	\$46,686
	<b>1.3.6.2</b>	<b>Hardware Construction</b>										
	<b>1.3.6.2.1</b>	<b>AMS and Road Warrior</b>										
	<b>1.3.6.2.1.1</b>	<b>Mezzanine Cards (RAM 1)</b>										
83%	1.3.6.2.1.1.2	Production	11/11/04	04/29/05	\$25,000	\$25,000	0.3	\$7,500	\$0	0	\$0	\$1,628
	<b>1.3.6.2.2</b>	<b>Hit Buffer</b>										
	<b>1.3.6.2.2.1</b>	<b>Mezzanine Cards (RAM 2)</b>										
44%	1.3.6.2.2.1.2	Production	03/01/05	05/16/05	\$25,000	\$25,000	0.3	\$7,500	\$0	0	\$0	\$5,362
0%	1.3.6.2.2.1.3	Testing	04/05/05	05/16/05	\$1,760	\$1,760	0.3	\$528	\$0	0	\$0	\$674

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	<b>1.3.6.2.2.2</b>	<b>Transition Cards</b>										
10%	1.3.6.2.2.2.3	Testing	03/15/05	04/11/05	\$880	\$880	0.3	\$264	\$0	0	\$0	\$303
74%	1.3.6.2.2.3	Pulsar Boards	11/04/04	05/20/05	\$63,924	\$63,924	0.3	\$19,177	\$0	0	\$0	\$6,365
	<b>1.3.6.2.3</b>	<b>Track Fitter</b>										
74%	1.3.6.2.3.2	Pulsar Boards	11/04/04	05/20/05	\$63,924	\$63,924	0.3	\$19,177	\$0	0	\$0	\$6,365